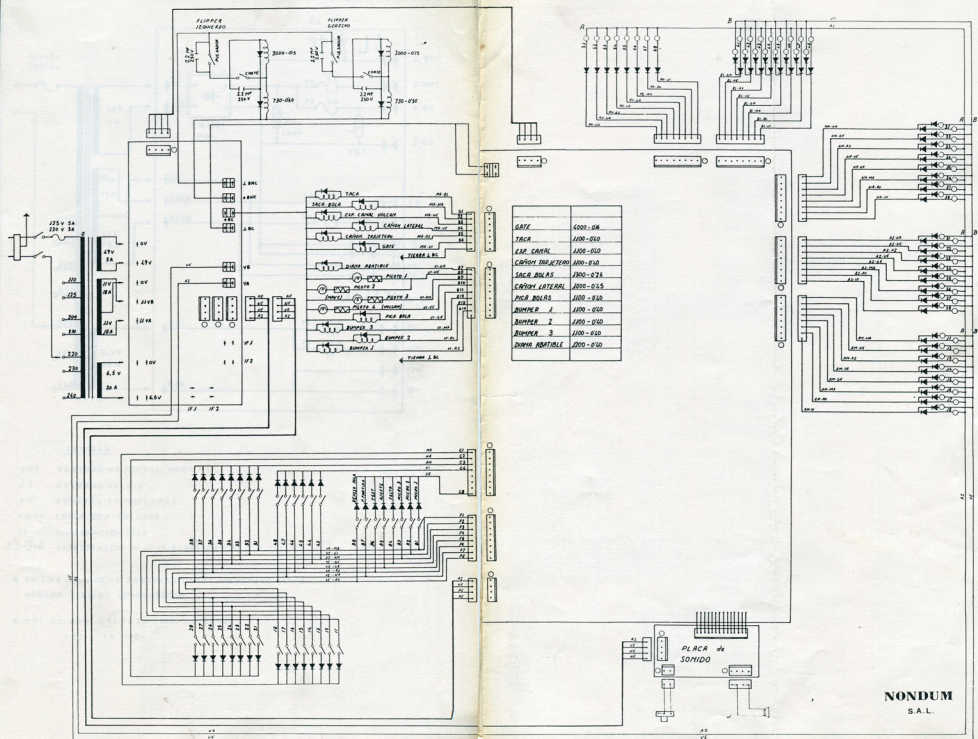
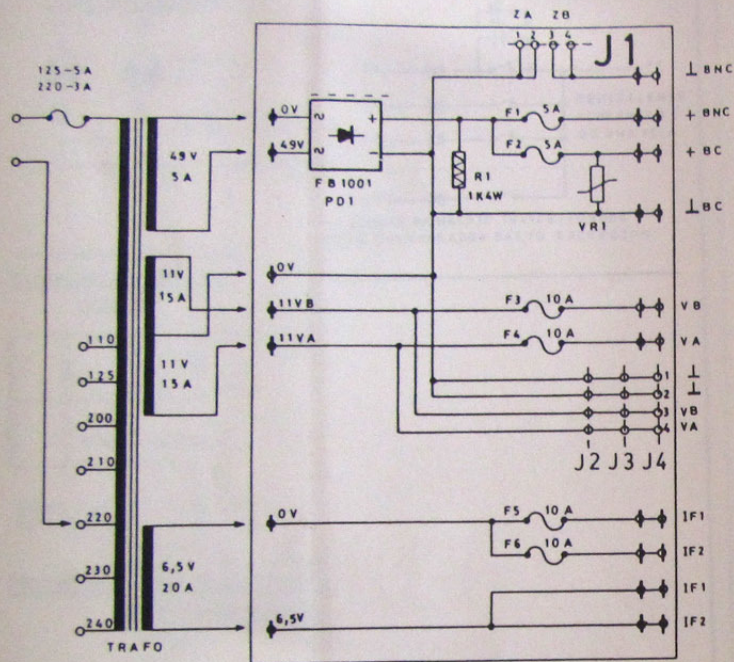




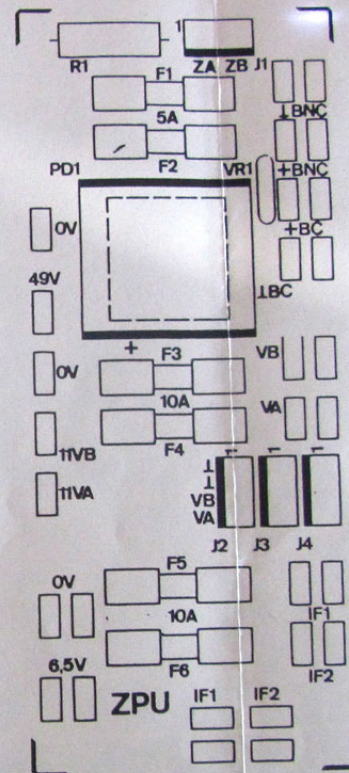


SALIDAS

- BNC BOBINAS NO CONTROLADAS
- J1 RETORNO RELE Z
- BC BOBINAS CONTROLADAS
- VB VA LUCES CONTROLADAS
- IF ILUMINACION FIJA
- J2-3-4 ALIMENTACION PLACAS ELECTRONICA

* EN LAS SALIDAS DE BOBINAS NO DEBE COLOCARSE JAMAS
NINGUN TIPO DE CONDENSADOR

* VR1 GE-MOV V82 ZA12 (50Vrms/66Vdc)
VOZ 7A 50K

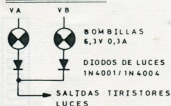


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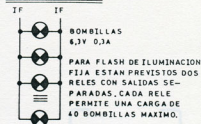
EFO_{SA} ZPU
POWER UNIT

PROYECTADO	7/2/86	J.GAMELL
DIBUJADO	8/6/86	J.YAGO

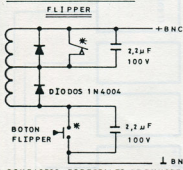
LUCE CONTROLADA



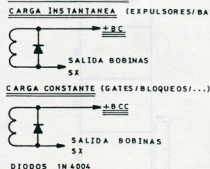
ILUMINACION FIJA



BOBINAS NO CONTROLADAS

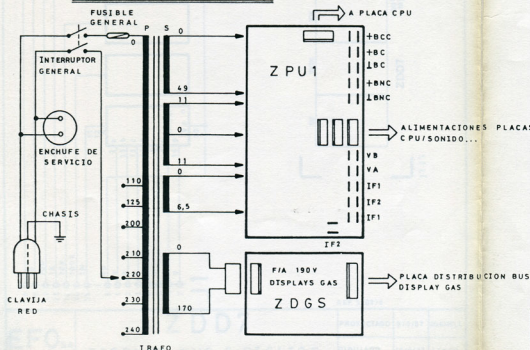


BOBINAS CONTROLADAS

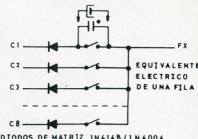
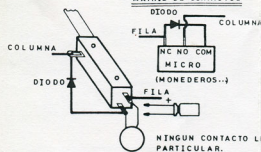


* CONTACTOS ESPECIALES DE TUNGSTENO

CONEXIONADO GENERAL DE POTENCIA



MATRIZ DE CONTACTOS



DIODOS DE MATRIZ 1N4148 / 1N4004

POTENCIOMETRO VOLUMEN SONIDO



SECCIONES CABLEADO

- 1) FASTONS (+BC / +BC / +BC / +BNC / VA / VB / IF...)
- 2) CONECTORES ALIMENTACION
- 3) ATAQUE BOBINAS
- 4) LUCES CONTROLADAS
- 5) MATRIZ DE CONTACTOS
- 6) CONECTOR DISPLAY A ZDBUS
- 7) CONECTORES PLACA CONTROL
- 8) CONTROL VOLUMEN

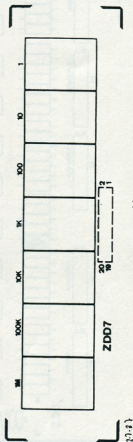
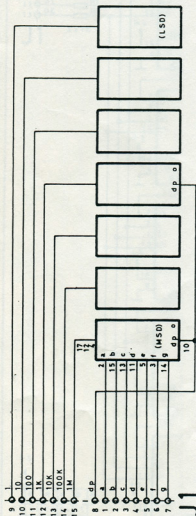
2,5 mm²
0,5 mm²
0,5 mm²
0,25 mm²
0,25 mm²
0,5 mm²
0,25 mm²
0,5 mm²
HILO BLINDADO

EFO_{SA}

ZCU 1
CONEXIONADO ELEMENTOS
PIN-BALL

PROYECTADO 25/9/86 J.GAMELL
DIBUJADO 24/4/87 J.YAGO

DISPLAY STATUS



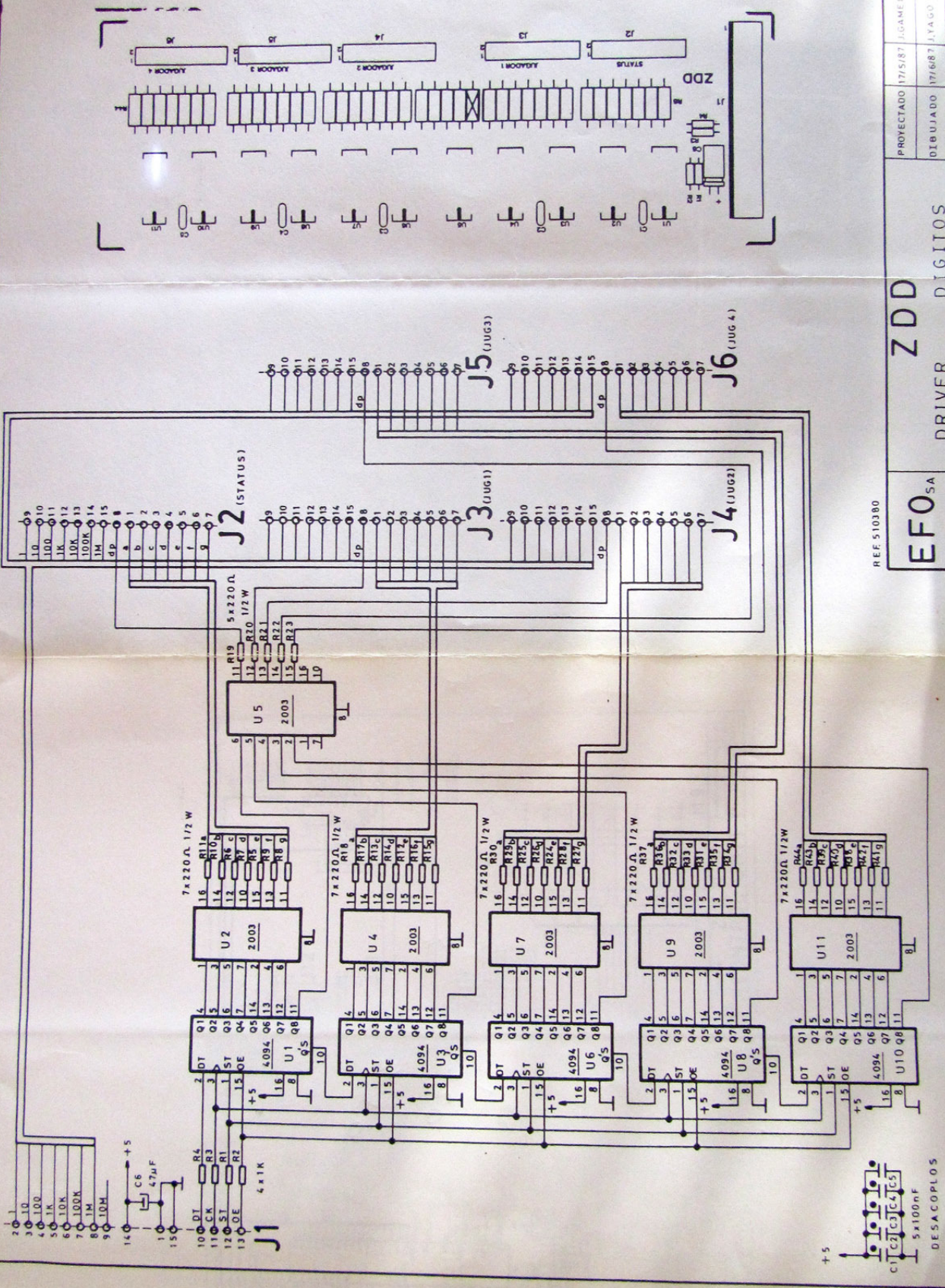
EFO_{SA}

ZDD7

DISPLAY BUS 7 DIGITOS

REF 510379

PROYECTADO	8/6/87	J.GAMELL
DIBUJADO	25/6/87	J.YAGO

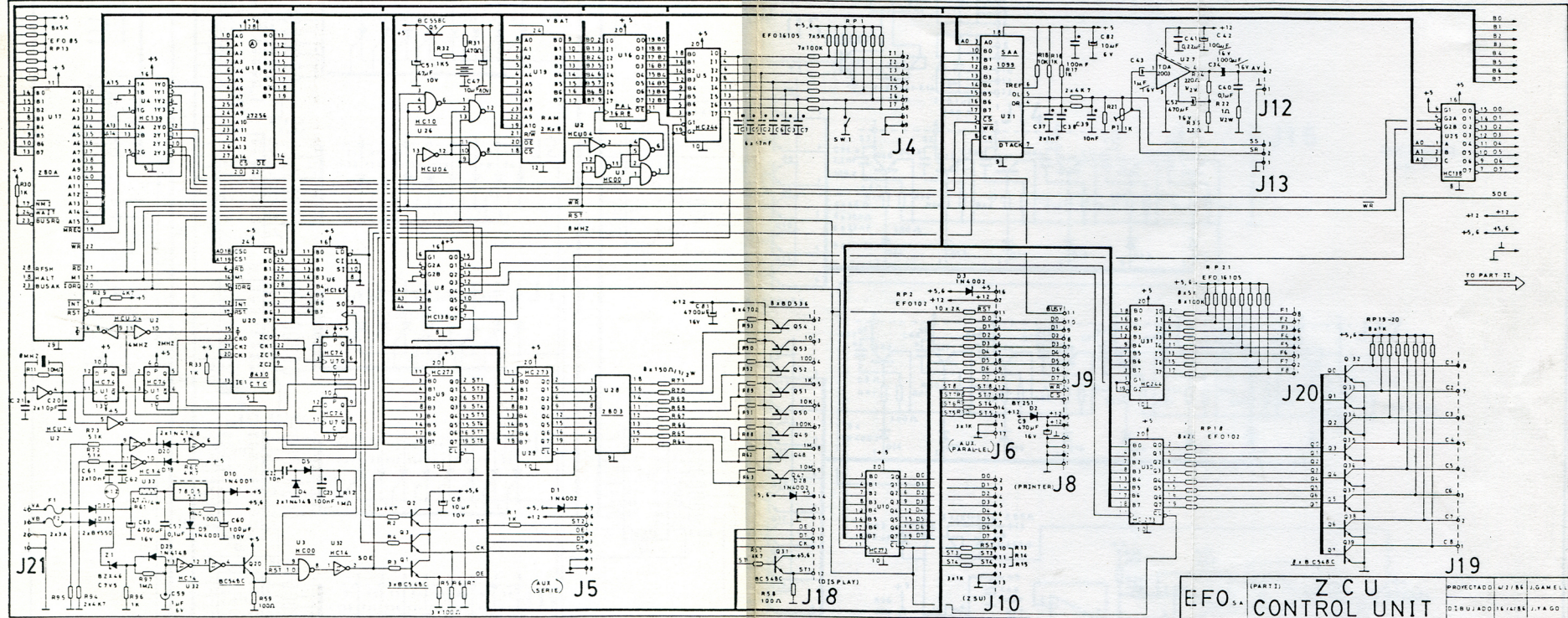


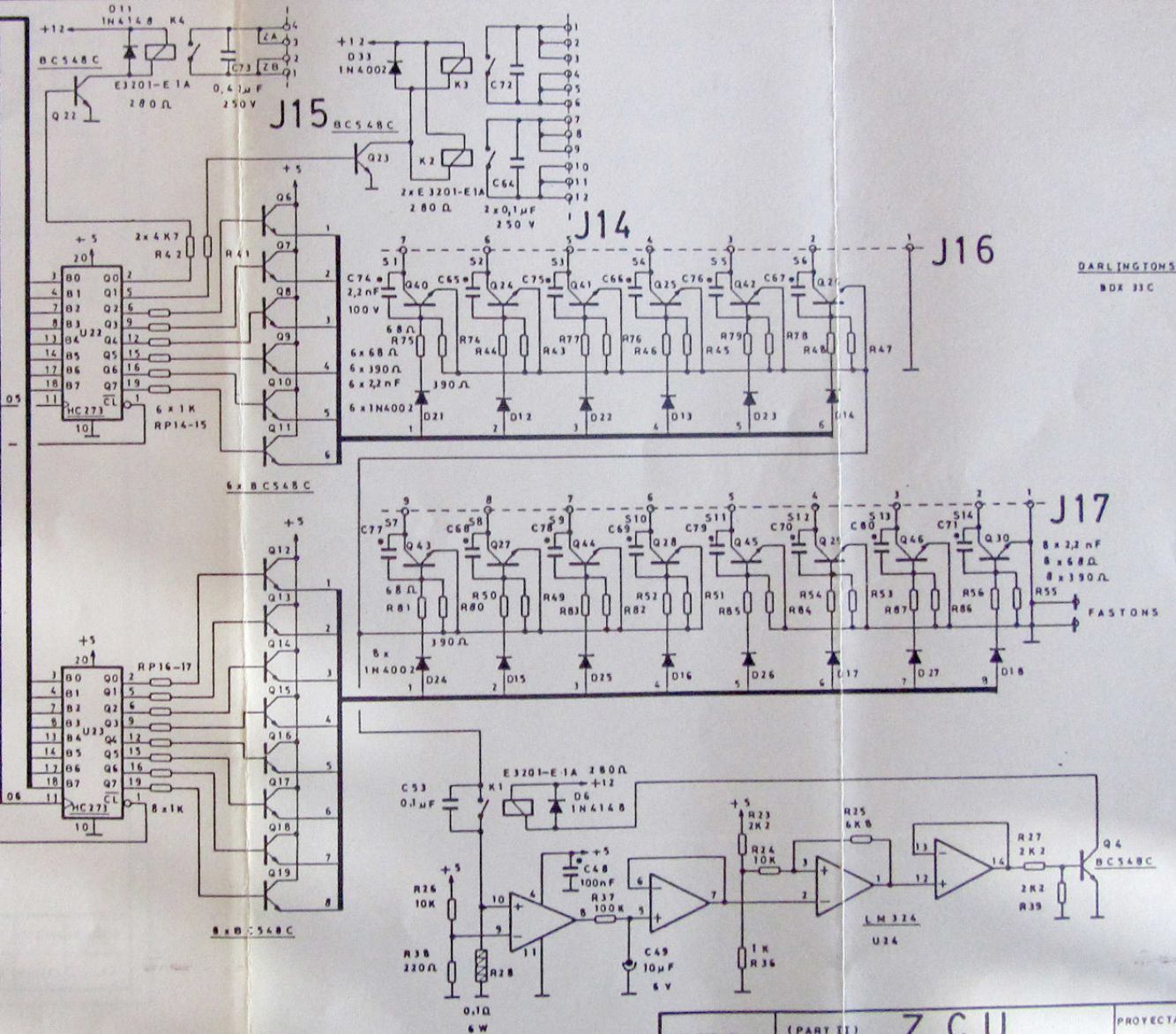
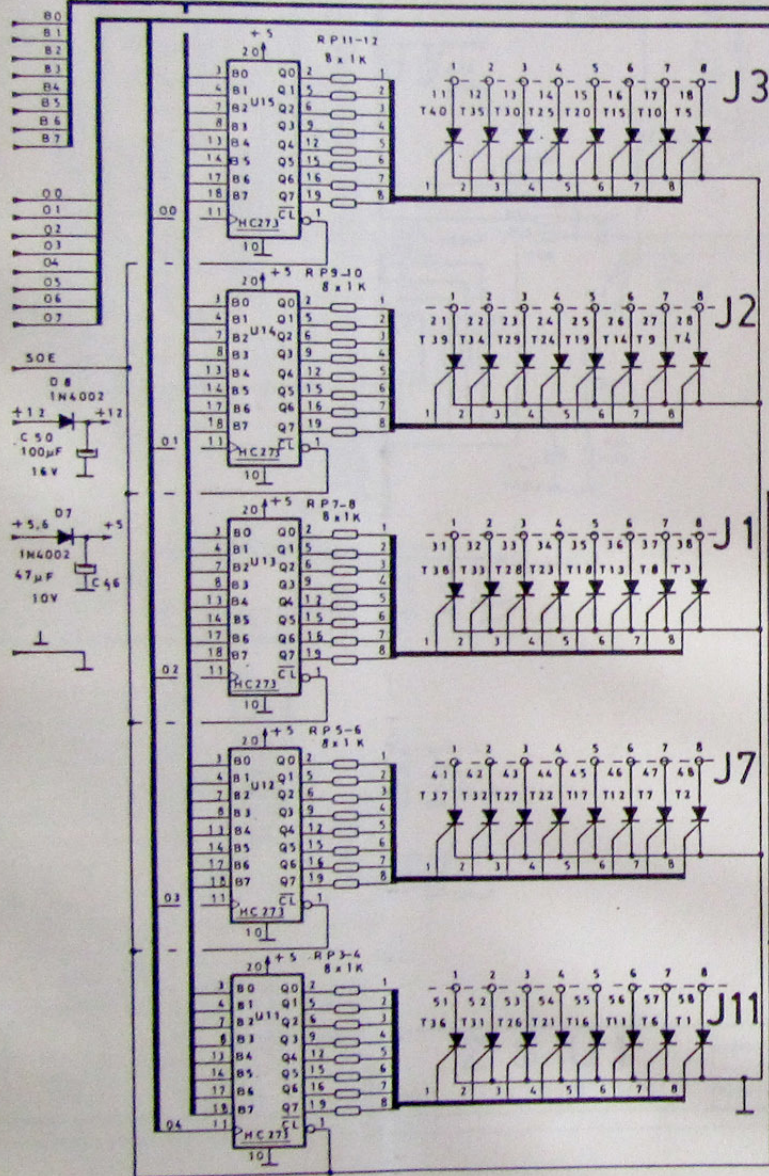
REF 510380

EFO ^{SA}	ZDD	DRIVER DIGITOS
PROYECTADO 17/5/87 J. GANELL	OTBUJADO 17/6/87 J. YAGO	

5x100nF

5A C6PLOS





DARLINGTONS
BOX 33C

FASTONS

EFO_{5A} (PART II) ZCU
CONTROL UNIT

PROYECTO 4/3/84 J. GARELL
DISEÑADO 22/4/84 J. YAGO

